REWORK PROCESS OF PATTERNED PHOTO-RESIST LAYER

This application claims the benefit of Taiwan patent application Serial No. **92118533**, filed July 7, 2003.

BACKGROUND OF THE INVENTION

5 Field of the Invention

[0001] The invention relates in general to a semi-conductor rework process of patterned photo-resist layer, and more particularly to a semi-conductor rework process of the patterned photo-resist layer on a dielectric anti-reflective coating (DARC).

10 Description of the Related Art

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[0002] Microfilming and etching are two common processes in semi-conductor manufacturing process. After going through sub-processes including dehydration bake, priming, photo-resist spinning, soft bake, exposure, post-exposure bake, development and hard bake, micro-filming process forms a patterned photo-resist layer on the dielectric anti-reflective coating of a substrate to facilitate the subsequent etching process. Moreover, the accuracy and reliability of patterned photo-resist layer has much to do with

the results of etching. Therefore, microfilming process really plays an important role in semi-conductor manufacturing process. When patterned photo-resist layer on DARC are found to have defects, e.g., over-etching or remnants on the surface of patterned photo-resist layer, instead of proceeding to etching process, a manufacturer would normally have the patterned photo-resist layer on DARC reworked. The description of the above process is disclosed below with accompanied drawings.

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[0003] Referring from FIG. 1A to 1D, sectional flowcharts of a conventional semi-conductor rework process of patterned photo-resist layer. In FIG. 1A, first of all, a substrate 12 is provided with dielectric anti-reflective coating (DARC) 14 being formed thereon. Of which, DARC 14, which is made of hydrophilic materials such as SiON or SiO₂, can be a SiON layer or can comprise an SiON layer and a SiO2 layer formed thereon. Next, a hexamethyldisilazane (HMDS) layer 16 is formed on DARC 14 through evaporation while the first patterned photo-resist layer 18 is formed on HMDS layer 16 as shown in FIG. 1B. Of which, HMDS layer 16, which is interchangeable with hydrophilic functional groups, an OH or an OOH functional group for instance, facilitates first patterned photo-resist layer 18 to be adhered to DARC 14 firmly. Hence HMDS layer 16 is also called a primer.

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If photo-resist manufacturing process needs to be reworked due to [0004] a failed micro-filming process or other factors, normally wet strip method or dry strip method is used to remove first patterned photo-resist layer 18 and HMDS layer 16. However, both strip methods always end up with many pin holes on the surface of DARC 14 as shown in FIG. 1C. The wet strip method, first of all, uses an acid agent consisted of HF and H2SO4 to remove first patterned photo-resist layer 18 and HMDS layer 16, wherein the acid agent erodes DARC 14 and leaves many pin holes containing ruminants of acid agent on the surface of DARC 14. Next, the wet strip method washes the surface of DARC 14 using an alkaline agent consisted of NH₄OH, H₂O₂ and de-ionized water to remove the particles adhered thereto. After the treatment of wet strip, the surface of DARC 14 will become hydrophilic because many hydrophilic functional groups such as OH, OOH and other functional groups are adhered onto the surface of DARC 14 after wash. The dry strip method, which uses oxygen plasma to remove first patterned photo-resist layer 18 and HMDS layer 16, also causes many pin holes on the surface of DARC 14, hence reducing the evenness thereof.

[0005] In the wake of the removal of first patterned photo-resist layer 18 and HMDS layer 16, the dry strip method forms another HMDS layer 26 on DARC 14 of FIG. 1C as shown in FIG. 1D through evaporation. Due to the

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adherence of hydrophilic functional groups other than OH functional group and OOH functional group onto the surface of DARC 14, HMDS layer 26 can only slightly reduce the hydrophilie on the surface of DARC 14. However, the surface of DARC 14 and HMDS 26 are still disposed to be hydrophilic, causing the adherence of the reworked patterned photo-resist layer to be reduced. Further due to the unevenness on the surface of DARC 14 or the remnants of acid agent contained in the pin holes thereon, the evenness on the surface of HMDS layer 26 formed on DARC 14 latter will be heavily influenced. When second patterned photo-resist layer 28 is formed on HMDS layer 26, the former can hardly be adhered onto the latter. Peeling will be easily experienced.

SUMMARY OF THE INVENTION

[0006] It is therefore an object of the invention to provide a reworked semi-conductor manufacturing process of patterned photo-resist layer. The design of forming the second DARC on the first DARC mitigates the unevenness and hydrophilie on the surface of the first DARC caused by strip method, hence preventing the patterned photo-resist layer reworked on the first DARC from peeling off.

[0007] It is therefore an object of the invention to provide a reworked

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semi-conductor manufacturing process of patterned photo-resist layer. First, a substrate is provided with the first DARC, the first primer and the first patterned photo-resist layer being sequentially formed thereon. Next, remove the first patterned photo-resist layer and the first primer from the first DARC. After that, form the second DARC on the first DARC; form the second primer on the second DARC. Last, form the second patterned photo-resist layer on the second primer.

[0008] It is therefore an object of the invention to provide a reworked semi-conductor manufacturing process of patterned photo-resist layer. First, a substrate is provided with the first SiON layer, the first SiO₂ layer, the first primer and the first patterned photo-resist layer being sequentially formed thereon. Next, remove the first patterned photo-resist layer and the first primer from the first SiO₂ layer. After that, form the second SiO₂ layer or the second SiON layer on the first SiO₂ layer; form the second primer on the second SiO₂ layer or the second SiO₂ layer or the second primer.

[0009] It is another object of the invention to provide a reworked semi-conductor manufacturing process of patterned photo-resist layer. First, a substrate is provided with the first SiON layer, the first primer and the first

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patterned photo-resist layer being sequentially formed thereon. Next, remove the first patterned photo-resist layer and the first primer from the SiON layer. After that, form the second SiON layer or a SiO₂ layer on the first SiON layer; form the second primer on the second SiON layer or the SiO₂ layer. Last, form the second patterned photo-resist layer on the second primer.

[0010] Other objects, features, and advantages of the invention will become apparent from the following detailed description of the preferred but non-limiting embodiments. The following description is made with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] FIG. 1A to 1D, sectional flowcharts of a conventional semi-conductor reworked manufacturing process of patterned photo-resist layer.

15 [0012] FIG. 2A to 2E, sectional flowcharts of the reworked semi-conductor manufacturing process of patterned photo-resist layer according to a preferred embodiment of the invention.

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DETAILED DESCRIPTION OF THE INVENTION

[0013] Referring first from FIG. 2A to FIG. 2E, sectional flowcharts of the rework process of a semi-conductor patterned photo-resist layer according to a preferred embodiment of the invention. In FIG. 2A, first of all, substrate 112 is provided with first dielectric anti-reflective coating (first DARC) 114, first primer 116 and first patterned photo-resist layer 118 being sequentially formed thereon, wherein first DARC 114 is made of hydrophilic materials. For example, first DARC 114 can be a SiON layer or can comprise the first SiON layer and the first SiO₂ layer formed on the first SiON layer. When photo-resist manufacturing process needs to be reworked due to a failed micro-filming process or other factors, the manufacturing process required includes removing first patterned photo-resist layer 118 and first primer 116 from first DARC 114. This can be done using either wet strip method or dry strip method.

[0014] The wet strip method, first of all, uses an acid agent consisted of HF and H₂SO₄ to remove first patterned photo-resist layer 118 and first primer 116 from first DARC 114. Next, the wet strip method uses an alkaline agent consisted of NH₄OH, H₂O₂ and de-ionized water to wash the surface of DARC 114. The surface of DARC 114, which becomes hydrophilic in the wake of

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the removal of first patterned photo-resist layer 118 and first primer 116, will become rougher or will even form many pin holes with remnants of acid contained within as shown in FIG. 2B. The dry strip method uses oxygen plasma to remove first patterned photo-resist layer 118 and first primer 116 from first DARC 114. The surface of first DARC 114 will be uneven or even have many pin holes formed thereon as shown in FIG. 2B.

- [0015] Next, form the second DARC 122 on first DARC 114 as shown in FIG. 2C wherein second DARC 122 fills up the pin holes on the surface of first DARC 114 and covers up first DARC 114 whose surface is hydrophilic.
- layer or another SiON layer; if the first DARC 114 comprises the first SiON layer and the first SiO₂ layer formed on the first SiON layer, the second DARC 122 can be the second SiO₂ layer or the second SiON layer. In short, the material of the second DARC 122 must be the same with or compatible with that of the first DARC 114, i.e., hydrophilic materials. Next, form the second patterned photo-resist layer 128 on the second primer 126 as shown in FIG. 2E.
 - [0017] However, anyone who is familiar with the technology of the invention is not to be limited thereto. For example, the material of primer 116

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and second primer 126 can both be hexamethyldisilazane (HMDS).

[0018] The rework process of patterned photo-resist layer disclosed in the above preferred embodiment forms the second DARC on the surface of the first DARC, which has been treated using strip method. Such design mitigates the unevenness and hydrophilie on the surface of the first DARC caused by strip method, hence preventing the second patterned photo-resist layer reworked on the first DARC from peeling off.

[0019] While the invention has been described by way of example and in terms of a preferred embodiment, it is to be understood that the invention is not limited thereto. On the contrary, it is intended to cover various modifications and similar arrangements and procedures, and the scope of the appended claims therefore should be accorded the broadest interpretation so as to encompass all such modifications and similar arrangements and procedures.